

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

Application: 09/328,910
(Reissue Application of U.S. Patent 5,637,913; issued June 10, 1997)

Filed: June 9, 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USING THE SAME, AND METHOD OF AND PROCESS FOR
FABRICATING THE TWO

Art Unit: 2811

Examiner: Unassigned (parent application's Examiner: J. Clark)

CERTIFICATE UNDER 37 CFR §3.73

Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

Hitachi, Ltd., a corporation located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, and Hitachi ULSI Systems Co., Ltd. (Formerly named Hitachi Microcomputer System Ltd.), a corporation located at 22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan certifies that they are joint assignees of the entire right, title and interest in the patent identified above by virtue of an Assignment (copy attached) which has been recorded in the U.S. Patent and Trademark Office at Reel 7070, Frame 578-579, and by virtue of a Change of Name document submitted concurrently herewith (under separate cover letter) for recordation (i.e., the Change of Name pertaining to a change in corporate name from "Hitachi Microcomputer System Ltd." to "Hitachi ULSI Systems Co., Ltd.")

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered and authorized to sign this certificate and consent on behalf of the assignee.

As stated also within the Form PTO/SB/53 filed concurrently herewith, the undersigned assignee of the entire interest in the patent identified above, hereby consents to the accompanying reissue application.

I declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true, and further state that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United

States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: Feb. 10, 2000

Hitachi, Ltd.

Yasuo Sakuta
Mr. Yasuo Sakuta

Executive Managing Director and
Patent Attorney
Intellectual Property Group
(Authorized Signing Officer)

Date: Feb 8, 2000

Hitachi ULSI Systems Co., Ltd.

Jinichiro Suzuki
Mr. Jinichiro Suzuki
TITLE: President

(Authorized Signing Officer)

Attachment:

copy of original Assignment
copy of cover letter and documents regarding
Change of Name

REISSUE APPLICATION DECLARATION BY THE INVENTOR

Docket Number (Optional)

501.32049R00

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 5,637,913, granted June 10, 1997, and for which a reissue patent is sought on the invention entitled Leadframe Semiconductor Integrated Circuit Device Using The Same, And Method Of And Process For Fabricating The Two, the specification of which

☐ is attached hereto.

☒ was filed on June 9, 1999 as reissue application number 09 / 328,910 and was amended on _____ (If applicable)

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

☐ by reason of a defective specification or drawing.

☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☐ by reason of other errors.

At least one error upon which reissue is based is described as follows:

Patentee claimed less than he had the right to claim as evidenced by the patented claims 1-14 being each directed to a "semiconductor integrated circuit device", whereas any of added claims 15-36 correct such oversight and claim more by each being alternatively directed to a "method of manufacturing a semiconductor device."

[Page 1 of 2]

Burden Hour Statement: This form is estimated to take 0.5 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 2)

Docket Number (Optional)
501.32049R00

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Donald R. Antonelli, Reg. No. 20,296; David T. Terry, Reg. No. 20,178; Melvin Kraus, Reg. No. 22,266; William I. Solomon, Reg. No. 28,565; Gregory E. Montone, Reg. No. 28,141; Ronald J. Shore, Reg. No. 28,577; Donald E. Stout, Reg. No. 26,422; Alan E. Schiavelli, Reg. No. 32,087; James N. Dresser, Reg. No. 22,973; Carl I. Brundidge, Reg. No. 29,621 and Paul J. Skwierawski, Reg. No. 32,173

Correspondence Address: Direct all communications about the application to:

☒ Customer Number

020457

OR

Type Customer Number here

Place Customer Number Bar
Code Label here

☐ Firm or
Individual Name

Address

Address

City

State

ZIP

Country

Telephone

Fax

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

I/We hereby claim benefit under Title 35, United States Code, §119 of any provisional application(s) and any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the foreign application(s) on which priority is claimed:

Provisional and/or Foreign Application(s)

Priority Claimed

4-320098

(Number)

Japan

(Country)

30 / 11 / 1992

(Day/Month/Year Filed)

☒

Yes

☐

No

4-71116

(Number)

Japan

(Country)

27 / 3 / 1992

(Day/Month/Year Filed)

☒

Yes

☐

No

(Number)

(Country)

(Day/Month/Year Filed)

☐

Yes

☐

No

I/We hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I/we acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 3)		Docket Number (Optional) 501.32049R00
Full name of sole or first inventor (given name, family name) Yujiro KAJIHARA		
Inventor's signature <i>Yujiro Kajihara</i>	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 3-4-8, Minamino, Hachioji-shi, Tokyo, Japan 2-8-15-401, Shibasaki-cho, Tachikawa-shi, Tokyo, Japan J. K		
Full name of second joint inventor (given name, family name) Kazunari SUZUKI		
Inventor's signature <i>Kazunari Suzuki</i>	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 4-20-10, Cyuuou, Oota-ku, Tokyo, Japan		
Full name of third joint inventor (given name, family name) Kunihiro TSUBOSAKI		
Inventor's signature <i>Kunihiro Tsubosaki</i>	Date January 29, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 2-29-22, Higashihiroyama, Hino-shi, Tokyo, Japan		
Full name of sole or 4th inventor (given name, family name) Hiromichi SUZUKI		
Inventor's signature <i>Hiromichi Suzuki</i>	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 2-19-5-201, Nishiazabu, Minato-ku, Tokyo, Japan		
Full name of 5th joint inventor (given name, family name) Yoshinori MIYAKI		
Inventor's signature <i>Yoshinori Miyaki</i>	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 1-3-17, Wakaba-cho, Tachikawa-shi, Tokyo, Japan		
Full name of 6th joint inventor (given name, family name) Takahiro NAITO		
Inventor's signature <i>Takahiro Naito</i>	Date January 26, 2000	
Residence Same as Post Office Address	Citizenship Japan	
Post Office Address 3-2-40-305, Gakuenhigashi-cho, Kodaira-shi, Tokyo, Japan		
<input checked="" type="checkbox"/> Additional joint inventors are named on separately numbered sheets attached hereto.		

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 4)		Docket Number (Optional) 501.32049R00
Full name of sole or 7th inventor (given name, family name) Sueo KAWAI		
Inventor's signature <i>Sueo Kawai</i>		
Residence Same as Post Office Address	Date <i>January 20, 2000</i>	
Post Office Address 1522-49, Ichinoya, Iwama-machi, Nishiibaraki-gun, Ibaraki, Japan	Citizenship Japan	
Full name of 8th joint inventor (given name, family name)		
Inventor's signature	Date	
Residence	Citizenship	
Post Office Address		
Full name of 9th joint inventor (given name, family name)		
Inventor's signature	Date	
Residence	Citizenship	
Post Office Address		
<input type="checkbox"/> Additional joint inventors are named on separately numbered sheets attached hereto.		